

10/15/01

Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

S.I.	O.I.P.E.	PATENT DATE
SCANNED <u>563</u>	Q.A. <u>MC</u>	

APPLICATION NO. 09/976052	CONT/PRIOR F	CLASS 327	SUBCLASS 333	ART UNIT 2816	EXAMINER M. Nguyen
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Hirokatsu Hayashi
Toshiro Takahashi

Level shift circuit and semiconductor integrated circuit

[illegible]

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<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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ISSUE FEE				
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